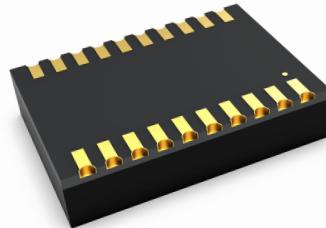


TD341S232H DFN package isolated RS232 Transceiver

Features

- Ultra-small, ultra-thin, chip scale DFN package
- Compliant with TIA/EIA-232 standard
- Integrated isolated 3.3V power
- I/O power supply range supports 3.3V microprocessors
- High isolation to 5000VDC
- Bus-Pin ESD protection up to 15kV(HBM)
- Baud rate up to 120kbps
- >25kV/us CMTI
- Industrial operating ambient temperature range: -40°C to +85°C
- Meet AEC-Q100 standards
- Meet EN62368 standards
- Moisture Sensitivity Level (MSL) 3

Package



Applications

- Industrial Automation
- Building Automation
- Smart Electricity Meter

Functional Description

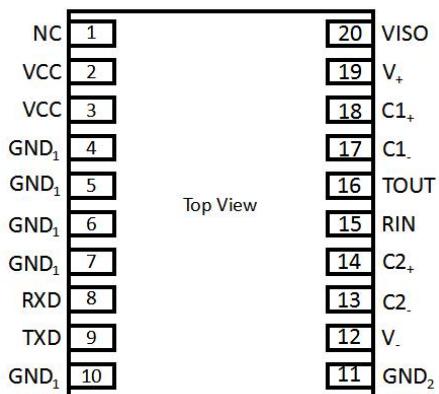
TD341S232H is a RS232 transceiver with low power consumption and high electrostatic protection and ESD protection, and it is fully compliant with TIA/EIA-232 standards. The main function of the product will be to convert the TTL level to the level of the RS232 protocol to achieve signal isolation. And the product comes with a constant voltage isolation power supply, which can achieve 5000VDC electrical isolation, and can also be easily embedded in user equipment, so that the equipment can easily realize the connection function of the RS232 protocol network.

TD341S232H focuses on strengthening the reliability design of T_{OUT} and R_{IN} pins and enhanced ESD design on the basis of traditional IC. Its T_{OUT} and R_{IN} port ESD tolerance is as high as 15kV (Human Body Model).

Contents

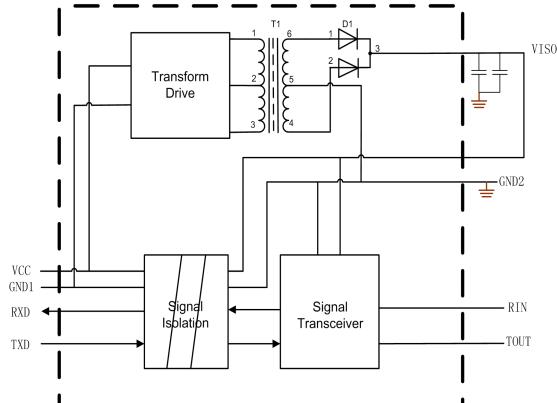
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Pin Connection



Note: All GND₁ pins are internally connected;
All GND₂ pins are internally connected.

Internal Block



Function Table

Letter	Description
H	High-Level
L	Low-Level

Table 1. Driver Function table

Transceiver function	Input	Output
Send function	TXD	T_OUT
	L	H
	H	L

Table 2. Receiver Function table

Transceiver function	Input	Output
Receive function①	R_IN	RXD
	$\geq 2.4V$	L
	$\leq 0.6V$	H
	$0.6V \leq RXD \leq 2.4V$	Uncertainty

Note : ①The receiving threshold varies slightly with Vcc.

Pin Descriptions

Pin Number	Pin Name	Pin Functions
1	NC	No function pin, can be left floating.
2	V _{CC}	Power supply. By using 1uF ceramic capacitance ground(GND ₁).
3	V _{CC}	Power supply. By using 1uF ceramic capacitance ground(GND ₁).
4	GND ₁	Logic side reference ground.
5	GND ₁	Logic side reference ground.
6	GND ₁	Logic side reference ground.
7	GND ₁	Logic side reference ground.
8	RXD	Receiver signal output pin.
9	TXD	Driver input pin.
10	GND ₁	Logic side reference ground.
11	GND ₂	Isolated output reference ground.
12	V-	Negative power generated internally, this pin is recommended to be connected to the isolated output reference ground(GND ₂) through a 0.1uF capacitor.
13,14	C2-,C2+	The positive and negative connections of the charge pump capacitor. These two pins are connected to an external capacitor C2, 0.1uF capacitor is recommended.
15	R _{IN}	Receiver input. This input accepts RS-232 signal level.
16	T _{OUT}	Drive output. This pin outputs the RS-232 signal level.
17,18	C1-,C1+	The positive and negative connections of the charge pump capacitor. These two pins are connected to an external capacitor C1, and a 0.1uF capacitor is recommended.
19	V+	Positive power generated internally, this pin is recommended to be connected to the isolated output reference ground(GND ₂) through a 0.1uF capacitor.
20	V _{ISO}	Isolated power output terminal, this pin must be connected to the isolated output reference ground(GND ₂) through a 1uF capacitor.

Absolute Maximum Ratings

General test conditions: Free-air, normal operating temperature range (Unless otherwise specified).

Parameters	Unit
Supply voltage	-0.3V to +3.5V
Driver input pin, TXD	-0.3V to +3.5V
Driver output pin, T _{OUT}	-13.2V to +13.2V
Receiver input pin, R _{IN}	-25V to +25V
Receiver output pin, RXD	-0.3V to +3.5V
Operating Temperature Range	-40°C to +105°C
Storage Temperature Range	-50°C to +150°C
Reflow Soldering Temperature	Peak temp. ≤250°C, maximum duration ≤60s at 217°C. Please also refer to IPC/JEDEC J-STD-020D. 3.

Important: Exposure to absolute maximum rated conditions for an extended period may severely affect the device reliability, and stress levels exceeding the "Absolute Maximum Ratings" may result in permanent damage. All voltage values are based on the reference ground(GND).

Recommended Operating Conditions

Symbol	Recommend an operate condition		Min.	Typ.	Max.	Unit
V _{CC}	Supply voltage		3.15	3.3	3.45	V
V _{IH}	High-level input voltage(TXD)		2		V _{CC}	
V _{IL}	Low-level input voltage(TXD)		0		0.8	
I _{OS}	Output current	Driver	2			mA
		Receiver			10	
R _L	Output load resistance			3000		Ω
T _A	Operating temperature range		-40		85	°C
-	Signaling rate				120	kbps

Electrical Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Driver						
V _{TOUT}	Drive output high level	R _L =3kΩ to GND ₂	5	7		V
	Drive output low level	R _L =3kΩ to GND ₂		-7	-5	V
R _{TOUT}	Driver output impedance		300			Ω
I _{tsc}	Driver short circuit current				60	mA
Receiver						
V _{RIN}	Receiver input range		-25		25	V
V _{RIL}	Receiver input low threshold voltage		0.6	0.9		V
V _{RIH}	Receiver input high threshold voltage			1.5	2.4	V
	Receiver input hysteresis			0.4		V
R _{RIN}	Receiver input impedance		3	5	7	kΩ
V _{ROH}	RXD high level output voltage		Vcc - 0.4	Vcc - 0.1		V
V _{ROL}	RXD low level output voltage				0.4	V
Power supply and safeguard characteristic						
I _{cc}	Supply current			25	35	mA
I _{cc}	Working current	No load		30	45	mA
		R _L =3kΩ to GND ₂		30	45	mA
ESD	HBM	T _{OUT} , R _{IN} to GND ₂			±15	kV
		Other pin			±2	kV
EFT	IEC61000-4-4	T _{OUT} , R _{IN} to GND ₂			±2	kV
SURGE	IEC61000-4-5	T _{OUT} , R _{IN} to GND ₂			±2	kV
VI-O	Insulate voltage				5000	VDC
	Insulate impedance		1			GΩ
	Insulate capacitance			50		pF
CMTI	Common mode transient immunity	TXD = V _{CC} or 0 V, V _{CM} = 1 kV, transient magnitude = 800 V	25			kV/us

Note : ESD indicators are non-charged test specifications, GND2 need to be connected to the earth during testing.

Transmission Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
-	Maximum data rate	Duty 40% ~ 60%			120	kbps
T _{PHL} , T _{PLH}	Driver propagation delay	R _L = 3kΩ to 7kΩ , C _L = 50pF			2	us
T _{PHL} , T _{PLH}	Receiver propagation delay	C _L = 15pF			2	us

Physical Specifications

Parameters	Value	Unit
Weight	1.0(Typ.)	g

Typical Performance Curves

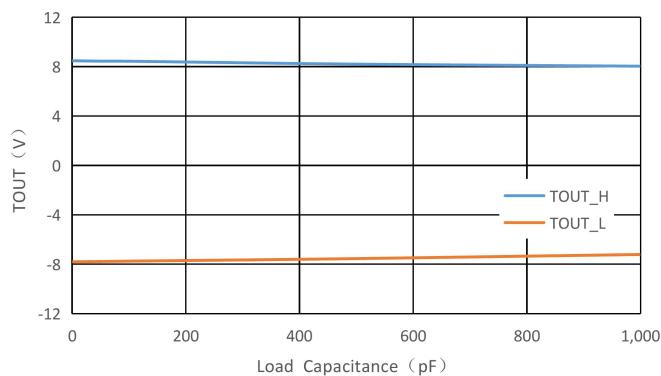


Figure 1. Transmitter Output Voltage High/Low VS Load Capacitance at 120 kbps

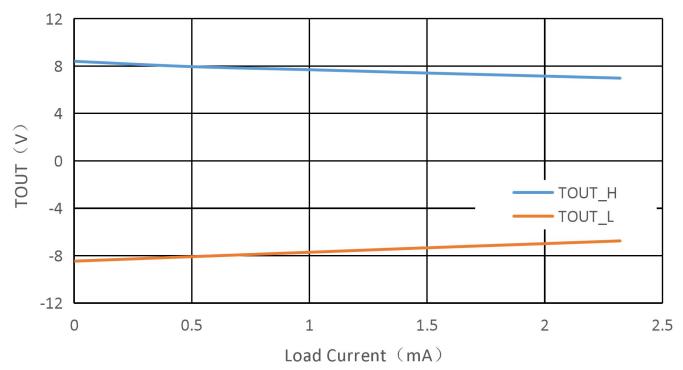


Figure 2. Transmitter Output Voltage High/Low VS Load Current

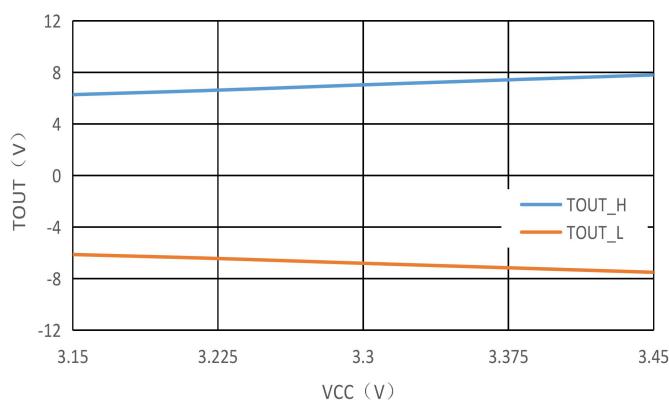


Figure 3. Transmitter Output Voltage High/Low VS V_{CC} , $R_L=3\text{ k}\Omega$

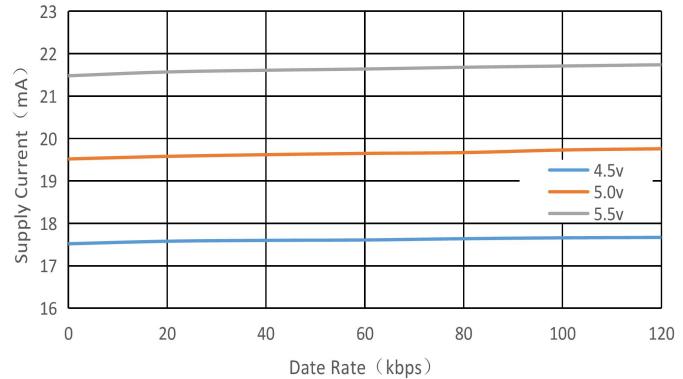


Figure 4. Supply current VS Data rate

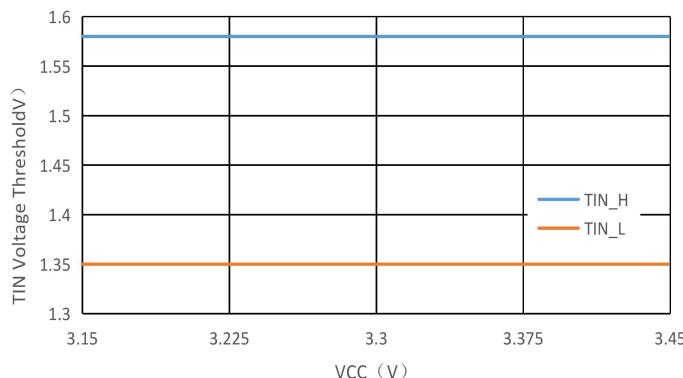


Figure 5. TIN Voltage Threshold VS V_{CC}

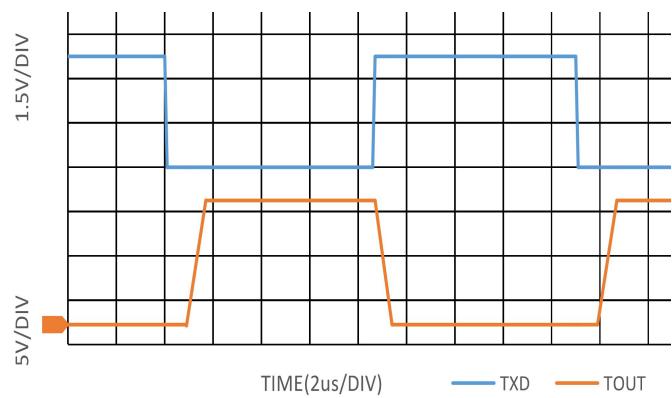


Figure 6. 120kbps Date Transmission($V_{CC}=3.3V$, $R_L=3\text{ k}\Omega$)

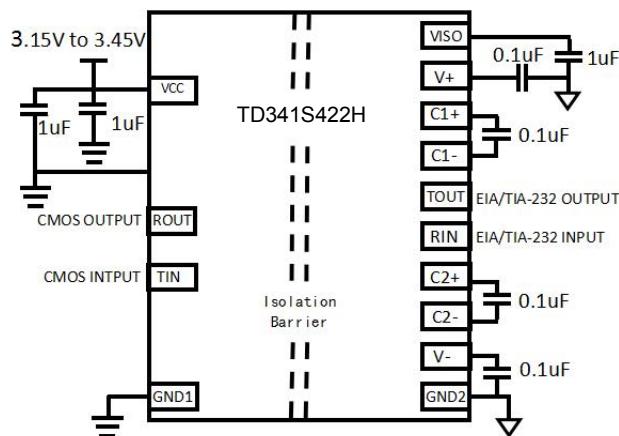


Figure 7. The typical model applies telephone

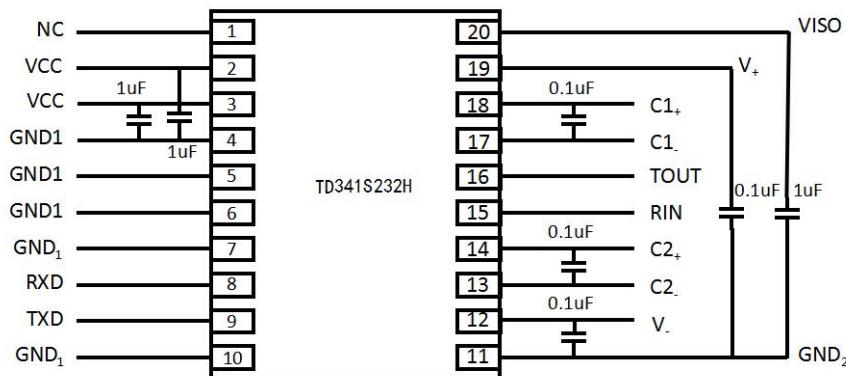


Figure 8. Type PCB layout

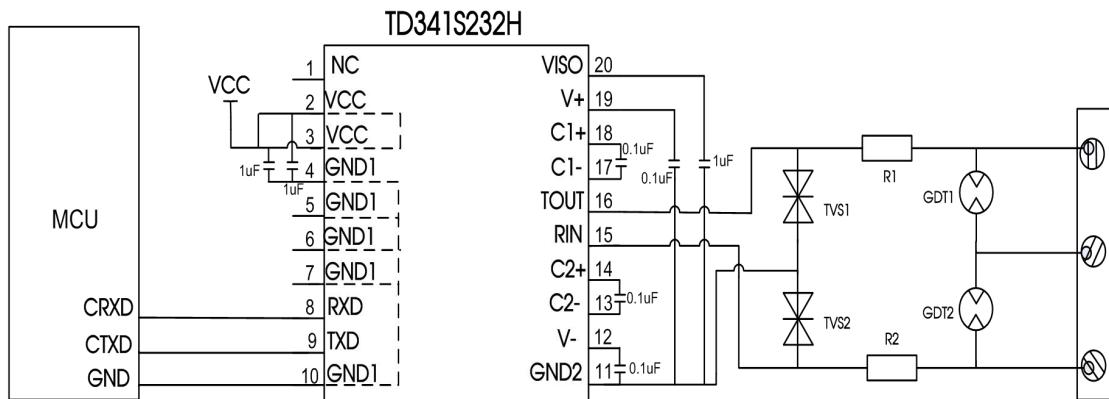


Figure 9. Port protection circuit for harsh environments

Recommended components and values:

Components	TD341S232H
TVS1, TVS2	SMCJ15CA
R1, R2	12Ω/2W(Wire-wound resistor)
GDT1, GDT2	S30-A90X

When the module is used in applications with harsh environment, it can be susceptible to large energy like lightning strike, etc. in which case, it is essential to add an adequate protection circuit to the 232 signal ports to protect the system from failure and maintain a reliable bus communication. Figure 9 provides a recommended protection circuit design for high-energy lightning surges, with a degree of protection related to the selected protection device. Parameter description lists a set of recommended circuit parameters, which can be adjusted according to the actual application situation. Also, when using the shielded cable, the reliable single-point grounding of the shield must be achieved.

Note: The recommended components and values is a general guideline only and must be verified for the actual user's application.

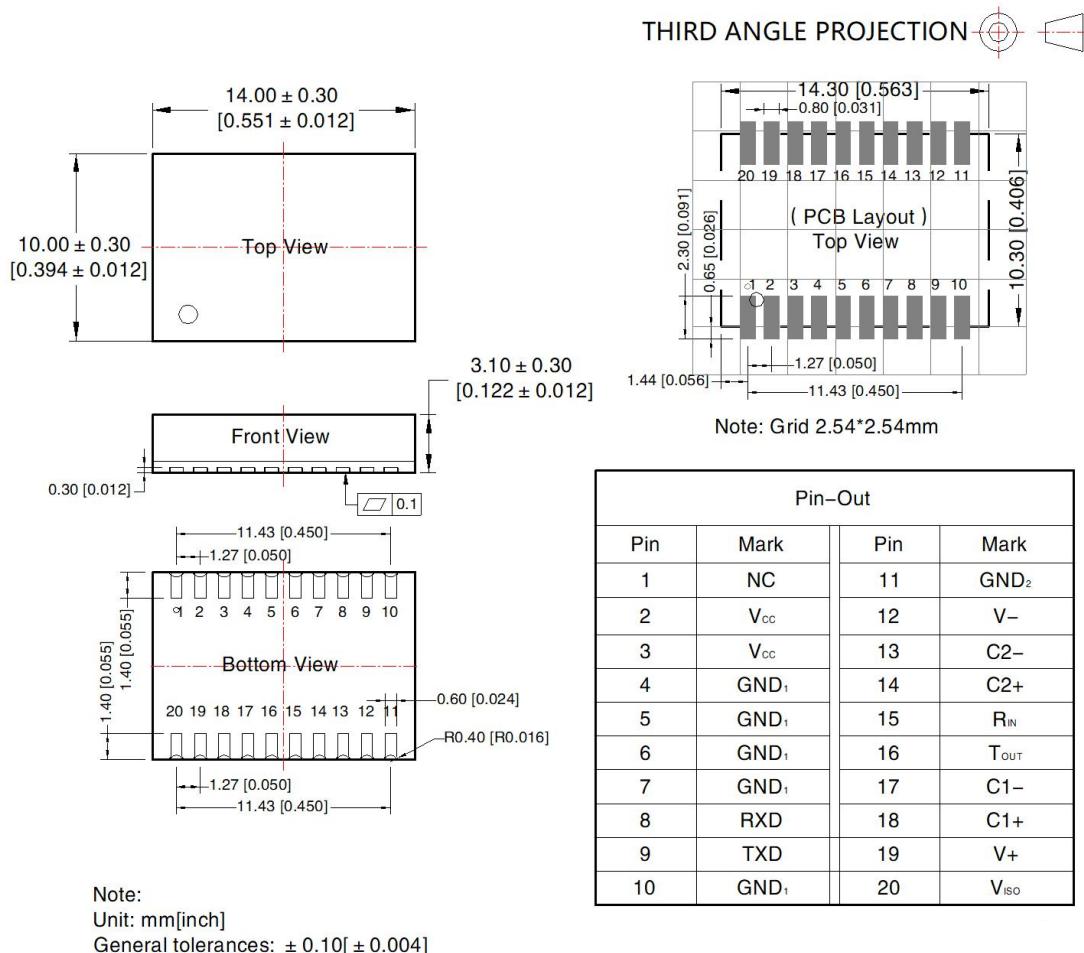
Recommendations

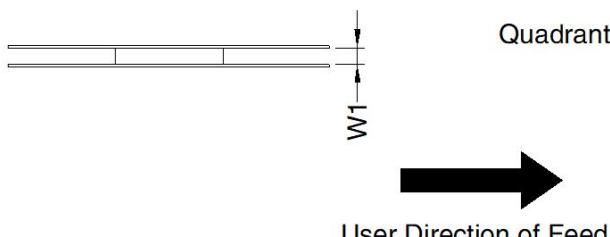
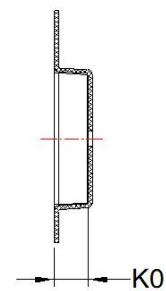
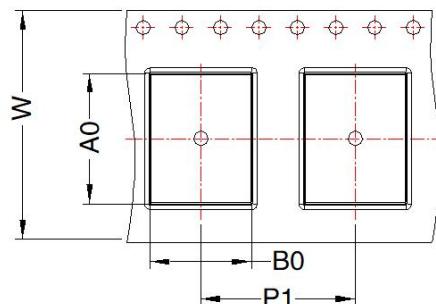
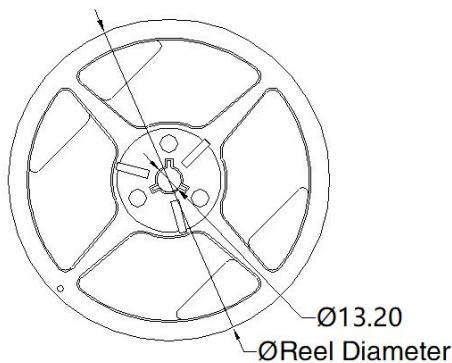
- ① The power supply is not recommended for other purposes, otherwise it may cause the bus voltage did not meet the requirements of communication, causes the communication failure.
 - ② Hot-swap is not supported.
 - ③ If the external input of TXD is insufficient, the pull-up resistor should be added according to the situation.
 - ④ Refer to *IPC 7093* for the welding process design of this product. For detailed operation guidance, please refer to *Hot Air Gun Welding Operation Instruction for DFN Package Product* or *Welding Operation Instruction for DFN Package Product*.

Ordering Information

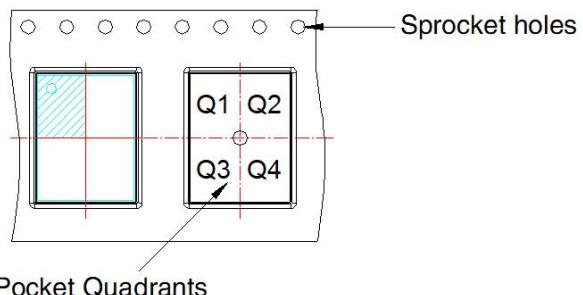
Part number	Package	Number of pins	Product marking	Tape & Reel
TD341S232H	DFN	20	TD341S232H	300/REEL

Package Information





Quadrant assignments for PIN 1 orientation in tape



Device	Package Type	Pin	MPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
TD341S232H	DFN 10x14	20	300	180.0	24.4	14.52	10.52	3			1

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